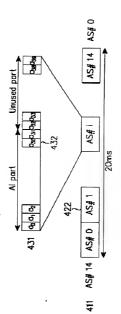


FIG. 4



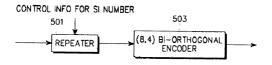


FIG. 5

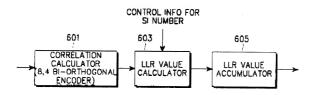
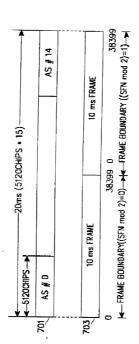


FIG. 6



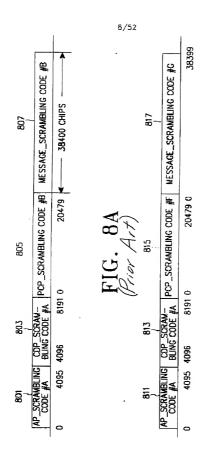


FIG. 8B

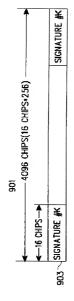


FIG. 9A

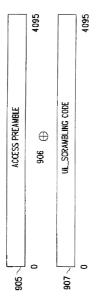
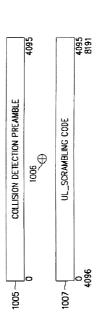


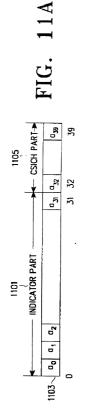
FIG. 9B

FIG. 10A

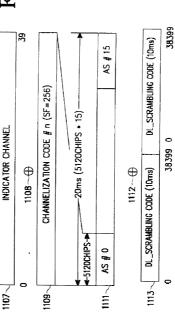


FIG. 10B









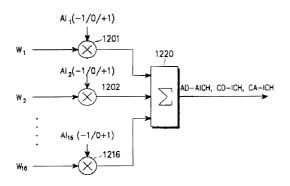


FIG. 12

FIG. 13A

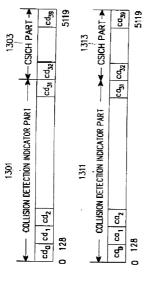
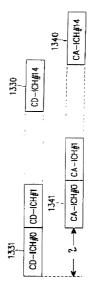


FIG. 13B



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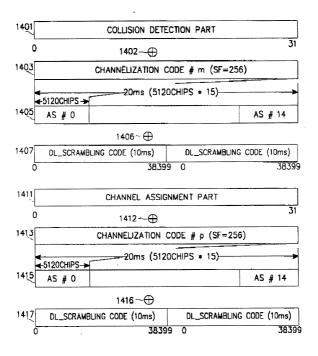


FIG. 14

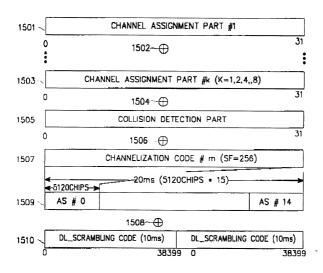


FIG. 15

FIG. 16

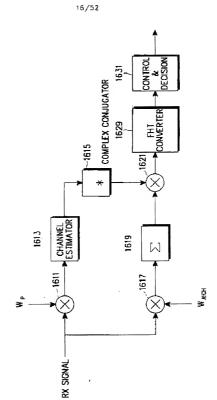
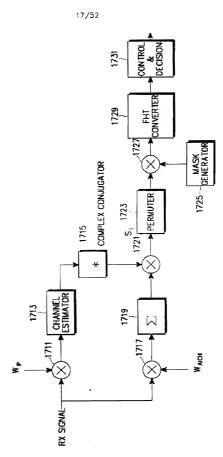


FIG. 17



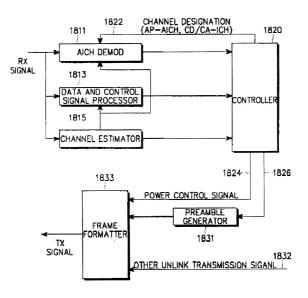


FIG. 18

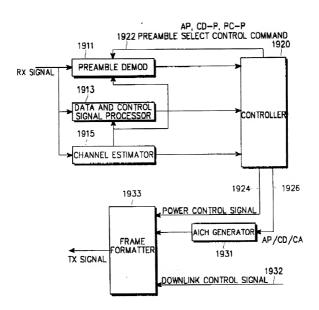
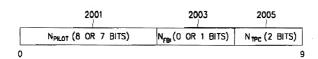


FIG. 19



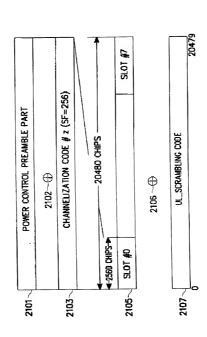
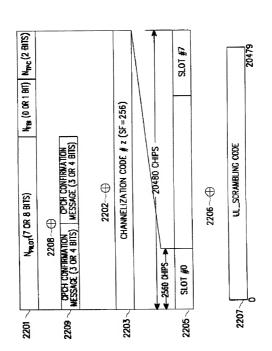


FIG. 22A



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FIG. 22B

T		38399			38399		 		38399
(38400 CHIPS) 2227	SCRAMBLING CODE #C1		2237	SCRAMBLING CODE #C2	E.		2247	SCRAMBLING CODE #CK	F.
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(4096 CHIPS)		0 1618 901				.,	 		•
(4096 CHIPS) (4096 CHIPS):	SCRAMBLING SCRAMBLING CODE #41 CODE #41	0 40954096					 		-
	UE #1			UE #2			 	¥	

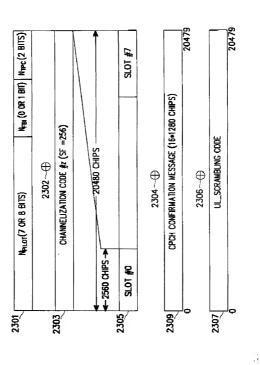
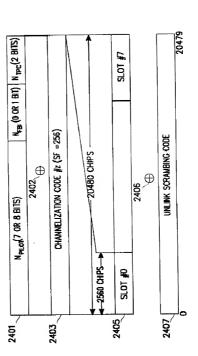
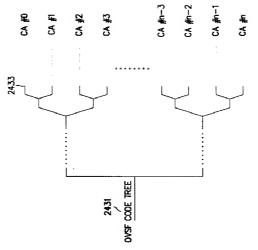


FIG. 24A



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FIG. 25A

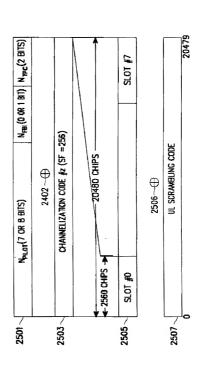
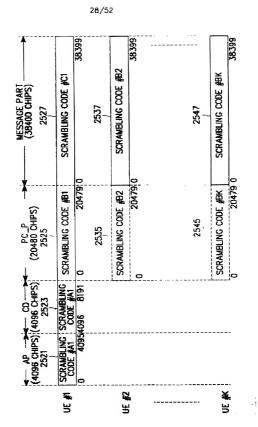
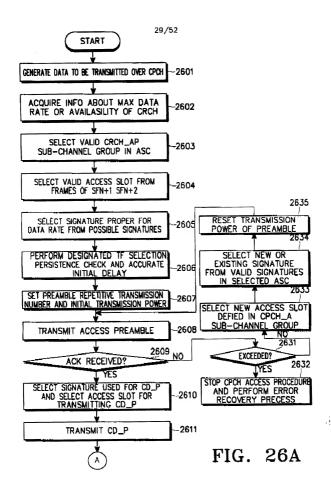


FIG. 25B





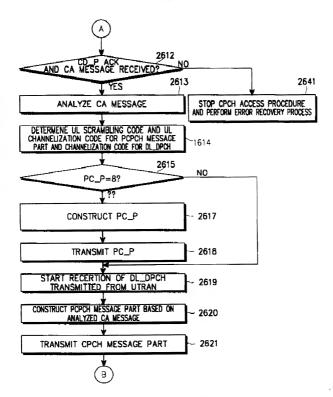


FIG. 26B

FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UE SIDE)

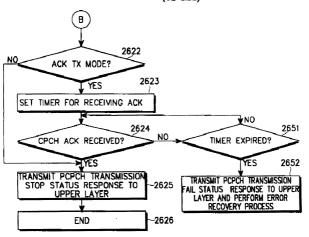


FIG. 26C

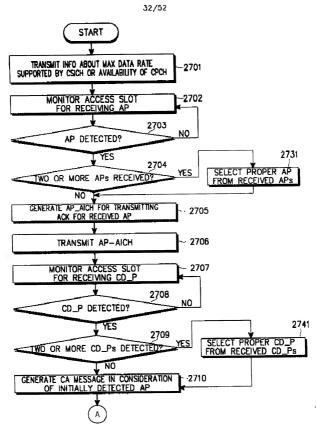


FIG. 27A

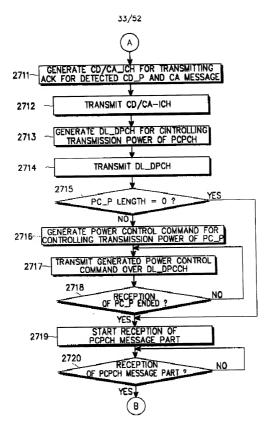


FIG. 27B

FLOWCHART OF CPCH USING CHANNEL ASSIGNMENT (UTRAN SIDE)

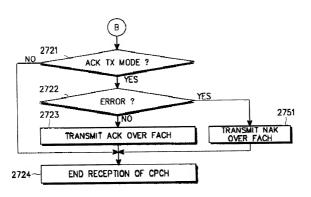


FIG. 27C

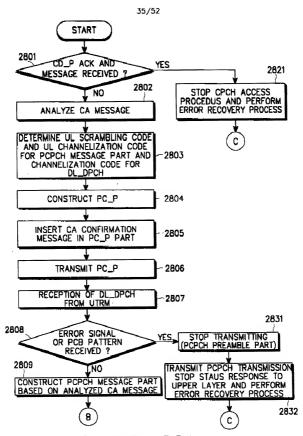


FIG. 28A

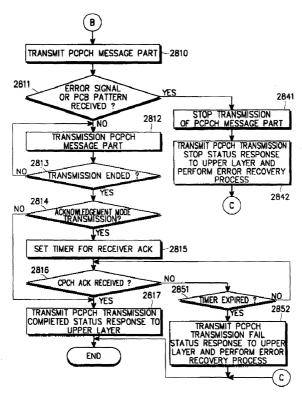
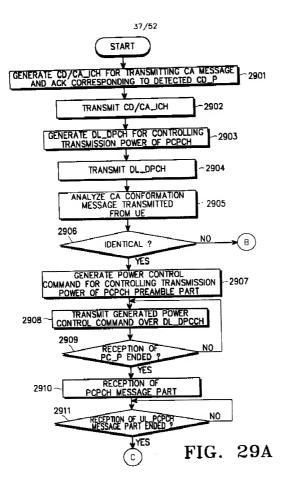


FIG. 28B



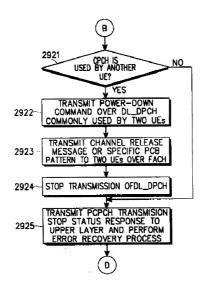


FIG. 29B

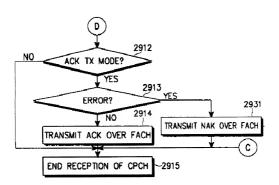


FIG. 29C

d

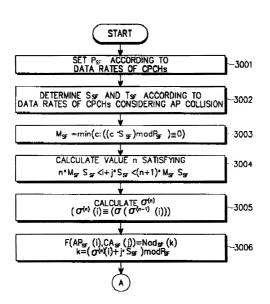


FIG. 30A

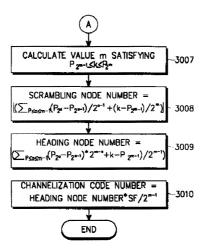


FIG. 30B

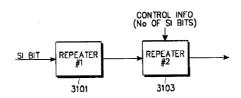
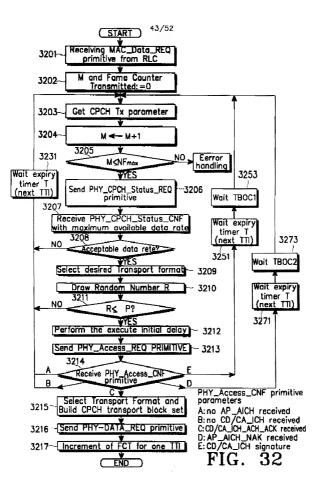


FIG. 31



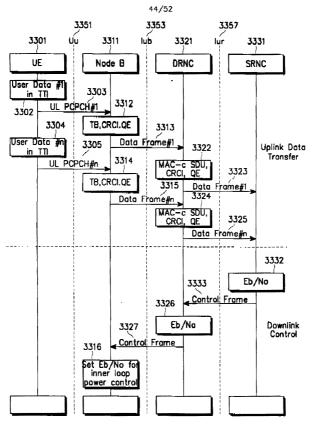


FIG. 33

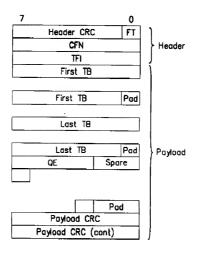


FIG. 34

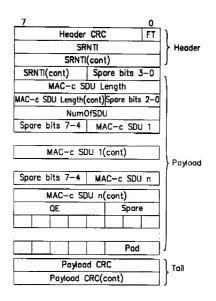


FIG. 35

1 :

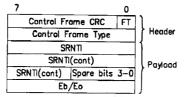


FIG. 36

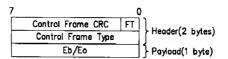


FIG. 37

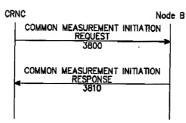


FIG. 38

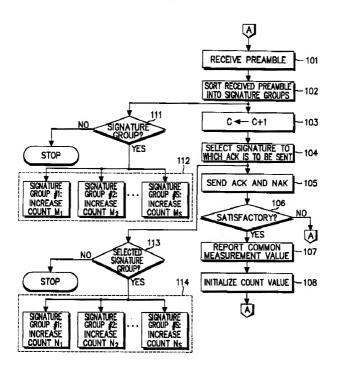


FIG. 39



FIG. 40



FIG. 41

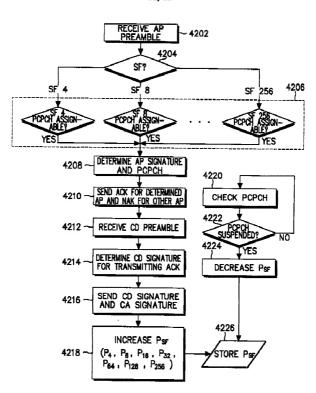


FIG. 42